|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| **LEADLAG - in case of AC INPUT( SINE WAVE )** | | | | | | |
| **Various Detected Faults in LEADLAG in the presence of faults with deviations ( >5%)** | | | | | | |
| **Injected Faults** | **=500, = 0.0420**  **Optimal Order – 18th** | | **=1000, = 0.0420**  **Optimal Order – 18th** | | **=2000, = 0.0420**  **Optimal Order – 18th** | |
| **No. of Coefficients Out of Bound** | **Fault Detection Status** | **No. of Coefficients Out of Bound** | **Fault Detection Status** | **No. of Coefficients Out of Bound** | **Fault Detection Status** |
| R1 3%**↑** | 0 | **X** | 0 | **X** | 0 | **X** |
| R2 2% **↓** | 0 | **X** | 0 | **X** | 0 | **X** |
| R3 4% **↑** | 0 | **X** | 0 | **X** | 0 | **X** |
| C1 2% **↓** | 0 | **X** | 0 | **X** | 0 | **X** |
| C2 4% **↑** | 0 | **X** | 0 | **X** | 0 | **X** |
| R1 1%**↓** | 0 | **X** | 0 | **X** | 0 | **X** |
| R2 1%**↑** | 0 | **X** | 0 | **X** | 0 | **X** |
| R3 4% **↓** | 0 | **X** | 0 | **X** | 0 | **X** |
| C1 3% **↑** | 0 | **X** | 0 | **X** | 0 | **X** |
| C2 4% **↓** | 0 | **X** | 0 | **X** | 0 | **X** |